

Docket No.: 67162-023

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of : Customer Number: 20277
Tadato YAMAGATA : Confirmation Number:
Serial No.: : Group Art Unit:
Filed: September 24, 2003 : Examiner: Unknown
For: DYNAMIC ASSOCIATIVE MEMORY DEVICE

INFORMATION DISCLOSURE STATEMENT

Mail Stop IDS
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the documents listed on the attached form PTO-1449. It is respectfully requested that the documents be expressly considered during the prosecution of this application, and that the documents be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed within three months of the U.S. filing date OR before the mailing date of a first Office Action on the merits. No certification or fee is required.

The relevance of the references listed on attached Form 1449 is discussed in the present specification.

Serial No.:

Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 500417 and please credit any excess fees to such deposit account.

Respectfully submitted,

MCDERMOTT, WILL & EMERY



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INFORMATION DISCLOSURE CITATION IN AN APPLICATION (PTO-1449)			ATTY. DOCKET NO. 67162-023		SERIAL NO.		
			APPLICANT Tadato YAMAGATA				
			FILING DATE September 24, 2003		GROUP		
U.S. PATENT DOCUMENTS							
EXAMINER'S INITIALS	CITE NO.	Document Number Number-Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document		Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
	US	5,319,589	06/07/1994	Yamagata et al.			
	US	5,146,300	09/08/1992	Hamamoto et al.			
	US	5,126,968	06/30/1992	Hamamoto et al.			
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FOREIGN PATENT DOCUMENTS							
EXAMINER'S INITIALS	CITE NO.	Foreign Patent Document Country Code ³ -Number -Kind Codes (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Figures Appear	Translation	
						Yes	No
		JP 5-298891	11/12/1993		Corresponding to U.S. Patent No. 5,319,589		
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)							
EXAMINER'S INITIALS	CITE NO.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.					
		IEEE J.S.S.C. vol.27. No.12, Dec.1992 pp. 1927, Tadato Yamagata, et al., "A 288-kb Fully Parallel Content Addressable Memory Using a Stacked-Capacitor Cell Structure"					
		IEICE Transactions on Electronics, vol. E76-C, No.11, Nov.1993, pp.1657, Tadato Yamagata, et al. "A Bitline Control Circuit Scheme and Redundancy Technique for High-Density Dynamic Content Addressable Memories"					
		IEICE Transactions on Electronics, vol.E77-C, No.8, Aug.1994, pp.1377, Takeshi HAMAMOTO, et al. "A Flexible Search Managing Circuitry for High-Density Dynamic CAMs"					
EXAMINER				DATE CONSIDERED			

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered.
Include copy of this form with next communication to applicant.

1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.